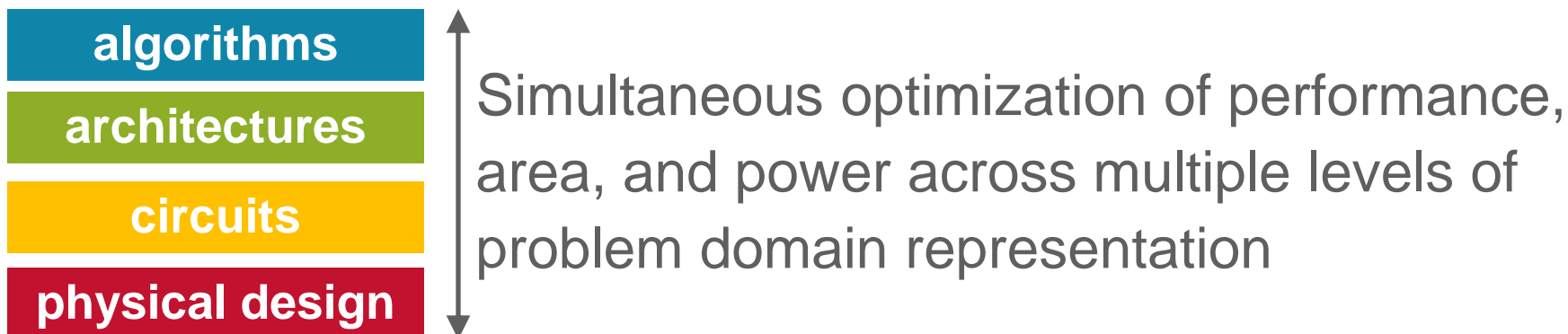


- Develop “challenging” digital signal processing modules for communication semiconductor products.
- Particular expertise in advanced forward error correction techniques such as Low-Density Parity-Check (LDPC) codes
- Deliver hardware modules and C++ models to business units for product integration e.g. LAN, WLAN, mmWave, ...



- Identify and share effective new methodologies across the company e.g. High Level Synthesis (C++ to logic gates)