

Steve Zingelewicz - About

Secure Architectures & Scalable Systems Lab - GE Global Research

zingelst@research.ge.com

Past Projects:

- Early dynamic websites
- Digital/Molecular Microscopy
- Real-time electrical switchgear control
- FPGAs and reconfigurable computing
- HW/SW co-simulation/performance estimation
- Large image compression/viewers
- Materials modeling acceleration
- Locomotive control software
- Medical image reconstruction acceleration (CT, US, MR)

Interests/Experience:

HW: GPUs, multicore CPUs, Cell processor, FPGA, HPC, microcontrollers

SW: C/C++, Python, Java, CUDA, MPI, OpenMP, Win32, graphics, game engines, concurrent systems, application optimization, software engineering, systems/performance engineering

I like making computers do things in the real world.